

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	307/108.ccls. and @pd>="20060127"	US-PGPUB; USPAT	OR	ON	2006/06/23 11:18
L2	5	307/109.ccls. and @pd>="20060127"	US-PGPUB; USPAT	OR	ON	2006/06/23 11:21
L3	3	307/110.ccls. and @pd>="20060127"	US-PGPUB; USPAT	OR	ON	2006/06/23 11:21
L4	20	363/41.ccls. and @pd>="20060127"	US-PGPUB; USPAT	OR	ON	2006/06/23 11:25
L5	13	363/127.ccls. and @pd>="20060127"	US-PGPUB; USPAT	OR	ON	2006/06/23 11:28
L6	2	363/133.ccls. and @pd>="20060127"	US-PGPUB; USPAT	OR	ON	2006/06/23 11:28
L7	2	363/147.ccls. and @pd>="20060127"	US-PGPUB; USPAT	OR	ON	2006/06/23 11:29
L8	44	(307/109,110 363/41,127,133,147). ccls. and @pd>="20060127"	US-PGPUB; USPAT	OR	ON	2006/06/23 11:29
L15	2	(307/109,110 363/41,127,133,147). ccls. and ((current adj (source supply)) and (capacitor capacitance condenser) and (switch transistor) and (voltage adj (detect\$3 sens\$3))) and @pd>="20060127"	US-PGPUB; USPAT	OR	ON	2006/06/23 11:40
L16	2	(307/109,110 363/41,127,133,147). ccls. and ((current adj (source supply)) and (capacitor capacitance condenser) and (switch transistor) and (voltage adj (detect\$3 sens\$3))) and @pd>="20060127"	US-PGPUB; USPAT	OR	ON	2006/06/23 11:41
L17	0	(307/109,110 363/41,127,133,147). ccls. and ((current adj (source supply)) and (capacitor capacitance condenser) and (switch transistor) and (voltage adj (detect\$3 sens\$3))) and (pulse\$width adj modul\$4) and @pd>="20060127"	US-PGPUB; USPAT	OR	ON	2006/06/23 11:39
L18	26	((current adj (source supply)) and (capacitor capacitance condenser) and (switch transistor) and (voltage adj (detect\$3 sens\$3))) and (pulse\$width adj modul\$4) and @pd>="20060127"	US-PGPUB; USPAT	OR	ON	2006/06/23 11:40

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L19	26	((current adj (source supply)) and (capacitor capacitance condenser) and (switch transistor) and (voltage adj (detect\$3 sens\$3))) and (pulse\$width adj modulat\$4) and @pd>="20060127"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 11:42
L20	0	((current adj (source supply)) and (capacitor capacitance condenser) and (switch transistor) and (voltage adj (detect\$3 sens\$3))) and (pulse\$width adj modulat\$4) and (pulse\$width adj modulat\$4).ti. and @pd>="20060127"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 11:40
L21	0	(pulse with generator) and (constant adj current adj (source supply)) and (modulated adj current adj (source supply)) and @pd>="20060127"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 11:43
L22	0	steele-andrew.in. and @pd>="20060127"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 11:43
L23	139	steele.in. and @pd>="20060127"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 11:43
L24	0	(modulated adj current) same (ramp adj circuit) and @pd>="20060127"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 11:44
L25	0	(modulated adj current) same (constant adj current) same (pulse\$width adj modulator) and @pd>="20060127"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 11:45
L26	0	(modulated adj current) and (constant adj current) and (pulse\$width adj modulator) and @pd>="20060127"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 11:45

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L27	38	(dc\$dc adj converter) same ((signal adj generator) or (pulse\$width adj modulat\$4)) and @pd>="20060127"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 11:49
L28	33	(dc\$dc adj converter) same ((signal adj generator) or (pulse\$width adj modulat\$4)) and (switch transistor) and @pd>="20060127"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 11:49
L29	34	(dc\$dc adj converter) same ((signal adj generator) or (pulse\$width adj modulat\$4)) and (switch transistor \$fet) and @pd>="20060127"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 11:49
L30	11	(dc\$dc adj converter) same ((signal adj generator) or (pulse\$width adj modulat\$4)) and (second adj (switch transistor \$fet)) and @pd>="20060127"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 11:50
L31	2	(dc\$dc adj converter) same ((signal adj generator) or (pulse\$width adj modulat\$4)) and (second adj (switch transistor \$fet)) and inverter and @pd>="20060127"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 11:51
L32	10	(dc\$dc adj converter) same ((signal adj generator) or (pulse\$width adj modulat\$4)) and inverter and @pd>="20060127"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 11:51
L33	5	(dc\$dc adj converter) same ((signal adj generator) or (pulse\$width adj modulat\$4)) and (push\$pull) and @pd>="20060127"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/23 11:51